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FPGA Developer - Warsaw, Poland

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Company: Global Trading Systems

Location: Warsaw

Category: computer-and-mathematical

Overview

We are looking to hire a senior hardware engineer to join a small and growing FPGA team working on developing components of an FPGA platform. There is huge scope in this role with a wide range of projects to work on. Candidates will be responsible for full stack systems and get exposure to many different layers of computing technology.

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Qualifications

5+ years of commercial FPGA or ASIC experience working as a hardware design engineer

Logic design expertise with HDLs such as Verilog, SystemVerilog, VHDL or SystemC

Familiarity with current FPGA architectures such as Xilinx Ultrascale+, Intel Stratix 10

Experience with 10/25/100GbE Ethernet, DMA, PCIe, NVMe, HBM, DDR, QDR

Experience with EDA tools from vendors such as Synopsis, Xilinx, Cadence, or Mentor Graphics

Experience with testing frameworks such as UVM, Cocotb a plus

Static timing analysis experience a plus

Linux driver experience a plus

C/C++/Python experience a plus

We are proud to employ some of the leading talent in the industry, and we work to ensure our employees enjoy a high quality-of-life.

We do not accept unsolicited headhunter and agency resumes and will not pay fees to any third-party agency or company that does not have a signed agreement with GTS.

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