

Senior Digital IC Design Engineer

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Company: ic resources

Location: Poland

Category: architecture-and-engineering

This is a superb opportunity for a Digital IC Design Engineer to join one of the world's leading high-tech companies in the Silicon space, based in Poland.

My client's main office is based in Warsaw, however hybrid / remote working within Poland is also a possibility for the right candidate. The Digital IC Design Engineer will join a talented ASIC team focussed on the very latest PCIe technology, working in cutting-edge technologies down to 2nm. Working in Verilog, the Digital IC Design Engineer will be involved in all aspects of the digital design sequence, including architecture definition, micro architectural specification, design, simulation, verification, and synthesis. You will be working on a leading flagship project, collaborating with teams around the world.

The successful Digital IC Design Engineer will have

A BSc or MSc degree in Computer Science / Electronic Engineering or related field

A number of years' prior experience within Digital ASIC / SoC Design, with solid Verilog coding skills

An understanding of digital architecture trade-offs for power, performance and area

Fluent language skills in English

This is a great chance to join a small, cohesive and very friendly team, part of a hugely successful global organisation, working on the VERY latest tech in this field. You can expect an excellent salary and very generous benefits package, plus annual bonus.

For more details please contact Caroline Pye.

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